WEST Search History

DATE: Saturday, March 13, 2010

Hide?	**Set Name**	**Query**	**Hit Count**

Prior Art

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

☐	L5	(717/$.ccls. or 714/$.ccls.) and (multi$1thread$4 or "multiple threads" parallel$4 multi$1) and (test$4 or debug$4 or analy$6 or diagnostic) same (embedded or mobile or hand$1held or real$2time) adj3 (processor or curcuit$4 or controller or cpu or processing hardware board port) same (input or output or incoming or outgoing or io or i/O)	280
☐	L3	(717/$.ccls. or 714/$.ccls.) and (multi$1thread$4 or "multiple threads" parallel$4 multi$1) and (test$4 or debug$4 or analy$6 or diagnostic) same (embedded or mobile or hand$1held or real$2time) adj3 (processor or curcuit$4 or controller or cpu or processing) same (input or output or incoming or outgoing or io or i/O) and (timestamp$4 or (time adj stamp $4))	24
☐	L2	kozio.asn.	2
☐	L1	(717/$.ccls. or 714/$.ccls.) and (multi$1thread$4 or "multiple threads" parallel$4 multi$1) and (test$4 or debug$4 or analy$6 or diagnostic) same (embedded or mobile or hand$1held or real$2time) adj3 (processor or curcuit$4 or controller or cpu or processing) same (input or output or incoming or I0 or i/O) and (timestamp$4 or (time adj stamp $4))	22

Interference

DB=PGPB,USPT,UPAD; PLUR=YES; OP=OR

| ☐ | L6 | (717/ $.ccls. or 714/ $.ccls.) and (multi $ 1thread $ 4 or "multiple threads" parallel $ 4 multi $ 1) and (test $ 4 or debug $ 4 or analy $ 6 or diagnostic) same (embedded or mobile or hand $ 1held or real $ 2time) adj3 (processor or curcuit $ 4 or controller or cpu or processing hardware board port) same (input or output or incoming or outgoing or io or i/O) | 290 |

END OF SEARCH HISTORY